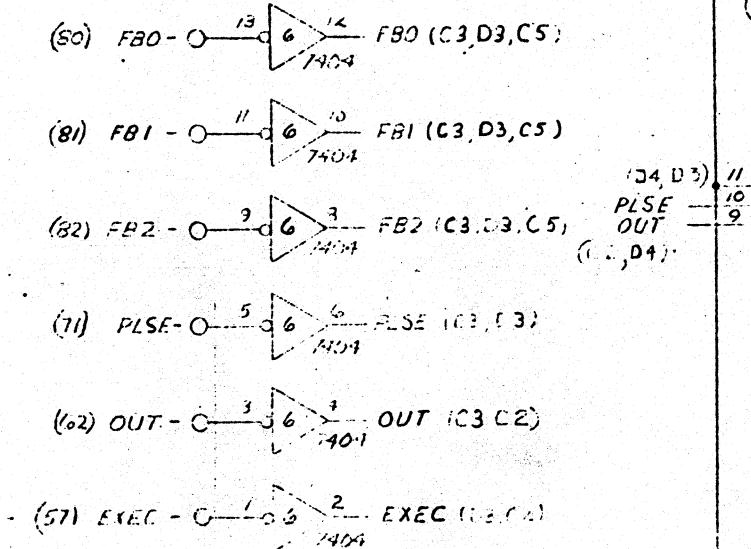


## CONTROL BUS RECEIVERS



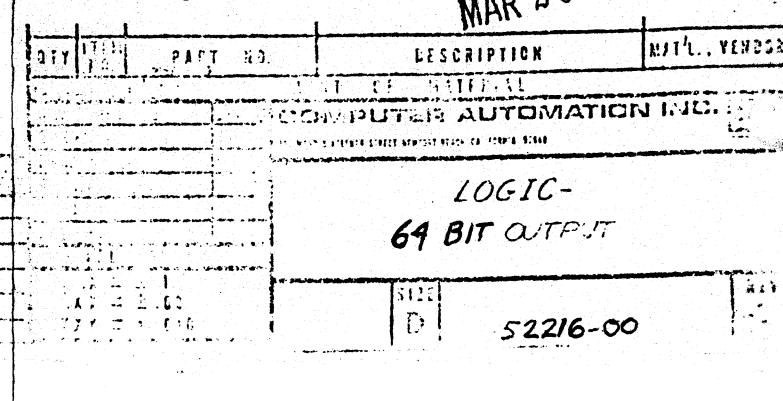
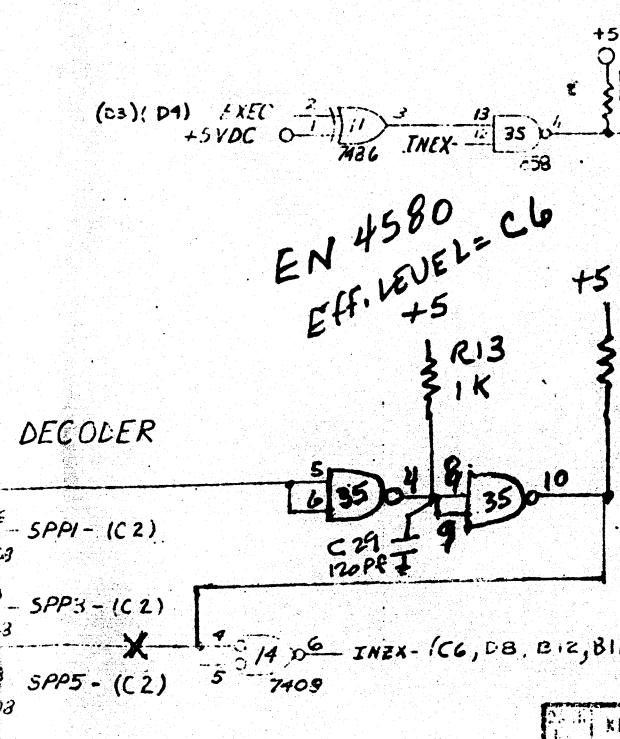
## DEVICE ADDRESS DECODER

## OUTPUT COMMAND DECODER

## SELECT COMMAND

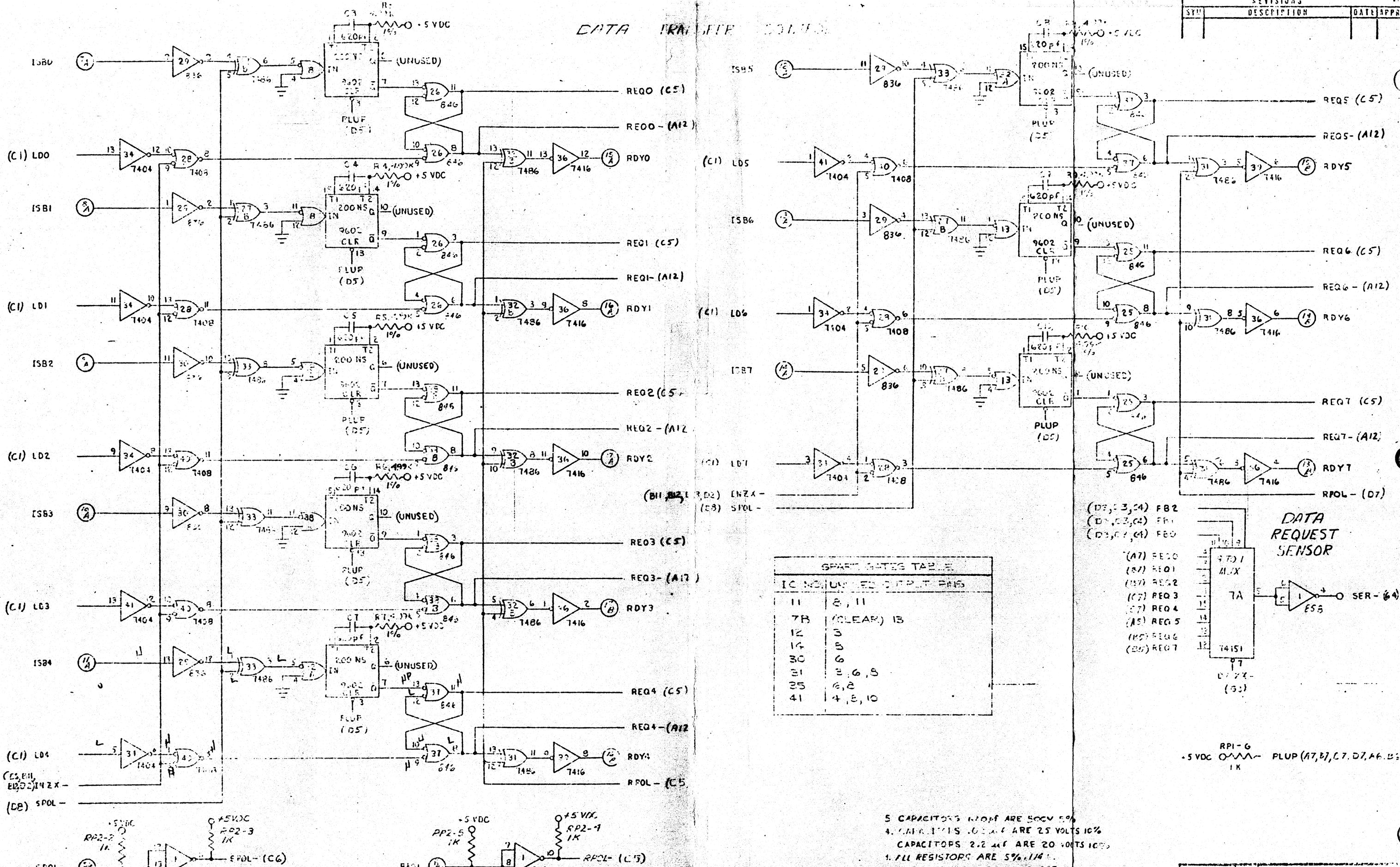
| IV. | VER                   | UPPER<br>BYTE<br>SELECTOR | ST#                   |
|-----|-----------------------|---------------------------|-----------------------|
| I   | I000 (A2,A16,A15,A14) | (A14,A15,A16,A2) ID00     | 5 816's               |
| I   | I001 (A2,A16,A15,A14) | (B2) ID08                 | UP0-<br>(C14,C15,C16) |
| I   | I002 (A2,A16,A15,A14) | (A14,A15,A16,A2) ID01     | UB1- (C14,C15,C16)    |
| I   | I003 (A2,A16,A15,A14) | (B2) ID09                 | UB1- (C14,C15,C16)    |
| I   | I004 (B2,B16,B15,B14) | (A14,A15,A16,A2) ID02     | UB2- (C14,C15,C16)    |
| I   | I005 (B2,B16,B15,B14) | (B2) ID10                 | UB2- (C14,C15,C16)    |
| I   | I006 (B2,B16,B15,B14) | (A14,A15,A16,A2) ID03     | UB3- (C14,C15,C16)    |
| I   | I007 (B2,B16,B15,B14) | (B2) ID11                 | UB3- (C14,C15,C16)    |
| I   | I008 (A2)             | (B14,B15,B16,A2) ID04     | UB4- (C14,C15,C16)    |
| I   | I009 (A2)             | (B2) ID12                 | UB4- (C14,C15,C16)    |
| I   | ID10 (A2)             | (A14,B15,B16,A2) ID05     | UB5- (D14,D15,D16)    |
| I   | ID11 (B2)             | (B2) ID13                 | UB5- (D14,D15,D16)    |
| I   | ID12 (B2)             | (B14,B15,B16,A2) ID06     | UB6- (D14,D15,D16)    |
| I   | ID13 (B2)             | (B2) ID14                 | UB6- (D14,D15,D16)    |
| I   | ID14 (B2)             | (B14,B15,B16,A2) ID07     | UB7- (D14,D15,D16)    |
| I   | ID15 (B2)             | (B2) ID15                 | UB7- (D14,D15,D16)    |
|     |                       | (C3,D4) OUT               | 816's                 |

EN 4580  
Eff. LEVEL = C6  
+5



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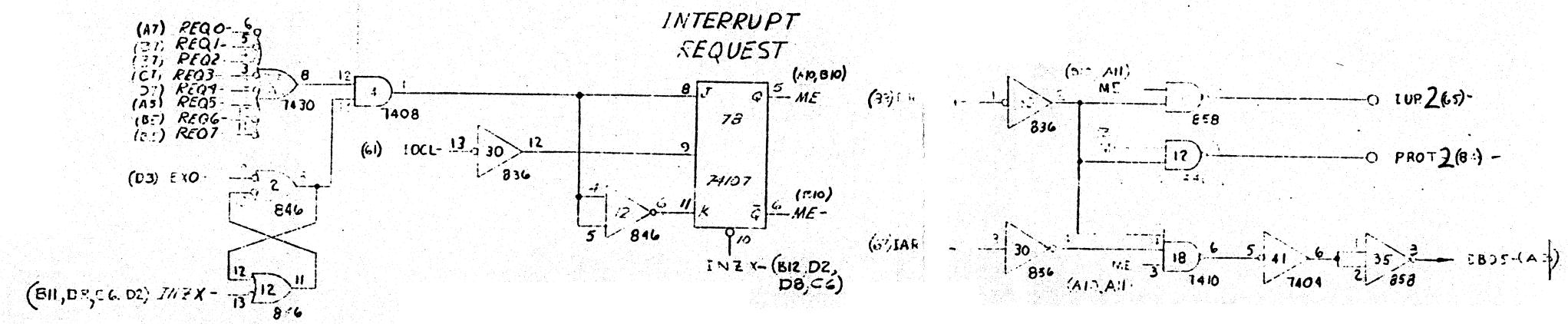
| SPARE GATES TABLE |                   |
|-------------------|-------------------|
| IC NO.            | UNUSED INPUT PINS |
| 11                | 2, 11             |
| 7B                | (CLEAR) 13        |
| 12                | 3                 |
| 14                | 5                 |
| 30                | 6                 |
| 31                | 2, 6, 8           |
| 25                | 6, 2              |
| 41                | 4, 5, 10          |

5 CAPACITORS 100PF ARE 50V 5%  
4. CAPACITORS 100PF ARE 25 VOLTS 10%  
CAPACITORS 2.2UF ARE 20 VOLTS 10%  
1. ALL RESISTORS ARE 5%, 1/4W.  
2. FOR ASSEMBLY DRAWING SEE 00-02216-C1  
1. REF. DESIGNATIONS NOT USED R1, R2

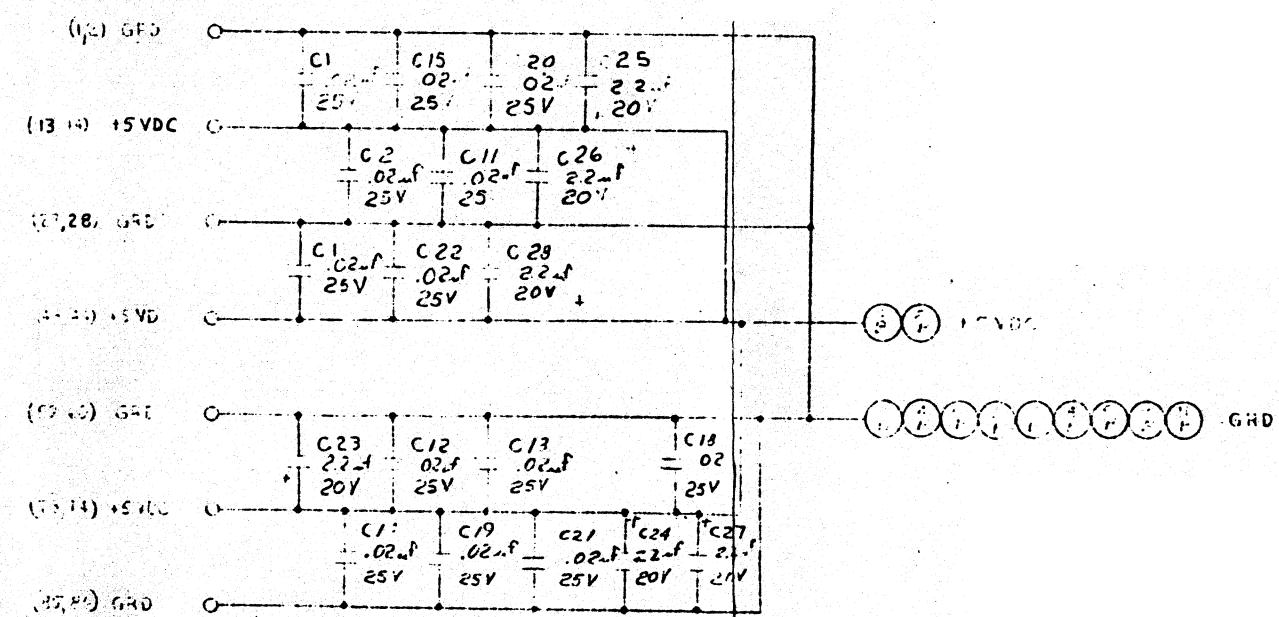
NOTES: L1-L5, J1-J4, K1-K4, L1-L5, J1-J4, K1-K4

D 52216-C1  
5 2 OF 4

## INTERRUPT REQUEST LOGIC



## INTERRUPT MASK



D 00-52216-00

SCALE = 1:1.3 OF 4